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Application No.	Applicant(s)	
10/749,467	THOMPSON ET AL.	
Examiner	Art Unit	
Tuan T. Nguyen	2824	
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6. ☐ Interview St Paper No./ 7. ☐ Examiner's 8. ☒ Examiner's 9. ☒ Other <u>Searc</u>	ummary (PTO-413), Mail Date Amendment/Comment Statement of Reasons for Allow	·
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DETAILED ACTION

Allowable Subject Matter

1. Claims 1-6 are allowed.

2. The following is an examiner's statement of reasons for allowance:

The prior art of record fail to disclose an apparatus, in combination with other cited limitations, comprising a write pointer circuit coupled to the demultiplexer circuit to provide a write pointer address for the selected bit location to the demultiplexer circuit; and being responsive to an input signal to set the write pointer address to a predetermined value; and a multiplexer circuit coupled to the register and to the write pointer circuit to provide a data output, the data output being at least one of a selected bit of a selected bit location in the register or a selected bit of the write pointer address as recited in claims 1 and 4.

Claims 2-3 and 5-6 are therefore allowed because of their dependency on claims 1 and 4; respectively.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

a. Kim et al. (US 6,853,588) disclose a first-in first-out memory circuit and method for executing same.

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b. Taylor (US 6,172,927) discloses a First-In First-Out integrated circuit memory device incorporating a retransmit function.

c. Iwakiri et al. (US 5,398,209) disclose a serial access memory with column address counter and pointers.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan T. Nguyen whose telephone number is (571) 272-1880. The examiner can normally be reached on Monday - Friday, 7:00 AM - 3:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Elms can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

June 26, 2005

Tuan T. Nguyen Patent Examiner Art Unit 2824

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ANH PHUNG EXAMINER